## **Refine Search**

## Search Results -

Terms	Documents	
(707/2   707/6).ccls.	2553	

US Pre-Grant Publication Full-Text Database

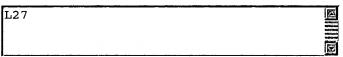
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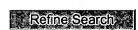
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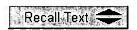
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Search:











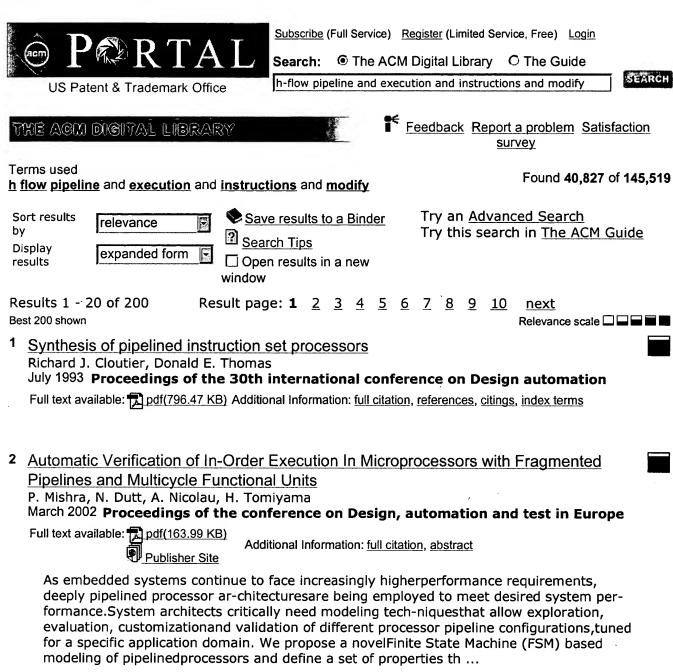
## **Search History**

DATE: Thursday, November 04, 2004 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set	
DB=USPT; PLUR=YES; OP=ADJ				
<u>L27</u>	707/2,6.ccls.	2553	<u>L27</u>	
<u>L26</u>	707/2,6.ccls.	2553	<u>L26</u>	
<u>L25</u>	717/139.ccls.	144	<u>L25</u>	
<u>L24</u>	L23 and l21	0	<u>L24</u>	
<u>L23</u>	717/127,128,130,131,132,154.ccls.	830	<u>L23</u>	
<u>L22</u>	L21 and pipelin\$ and execut\$	1	<u>L22</u>	
<u>L21</u>	(conjugat\$ near4 process\$) and (conjugat\$ near4 (map\$ or match\$))	57	<u>L21</u>	
<u>L20</u>	(conjugat\$ near4 processor) and (conjugat\$ near4 mapp\$)	0	<u>L20</u>	
<u>L19</u>	(conjugat\$ near4 processor) and (conjugat\$ near4 mapp\$)	0	<u>L19</u>	
DB=T	CDBD; PLUR=YES; OP=ADJ			
<u>L18</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	0	<u>L18</u>	
DB=L	OWPI; PLUR=YES; OP=ADJ			

<u>L17</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	0	<u>L17</u>
DB = 0	IPAB; PLUR=YES; OP=ADJ		
<u>L16</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	0	<u>L16</u>
DB=EPAB; PLUR=YES; OP=ADJ			
<u>L15</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	0	<u>L15</u>
DB = 0	USOC; PLUR=YES; OP=ADJ		
<u>L14</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	0	<u>L14</u>
DB=B	PGPB; PLUR=YES; OP=ADJ		
<u>L13</u>	(h-flow or h flow or conjugate) and pipeline and map\$ and entr\$ and trigger\$ and execut\$	30	<u>L13</u>
DB = 0	USPT; PLUR=YES; OP=ADJ		
<u>L12</u>	19 and map\$ and entr\$ and trigger\$	8	<u>L12</u>
<u>L11</u>	19 and conjugat\$	0	<u>L11</u>
<u>L10</u>	L9 and (trigger\$ near4 satisf\$)	0	<u>L10</u>
<u>L9</u>	(pipeline\$ near5 execut\$ near5 modif\$)	64	<u>L9</u>
<u>L8</u>	(conjugat\$) near5 pipeline\$ near5 (modif\$ or updat\$ or chang\$) and execut\$	0	<u>L8</u>
<u>L7</u>	L6 and execut\$	0	<u>L7</u>
<u>L6</u>	11 and pipeline near5 (chang\$ or modif\$ or alter\$)	6	<u>L6</u>
<u>L5</u>	11 and (trigger\$ near5 satis\$ neat6 execut\$)	0	<u>L5</u>
<u>L4</u>	11 and ((h-flow) or (conjugate flow)) near5 (code\$ or program\$)	0	<u>L4</u>
<u>L4</u> <u>L3</u>	11 and ((h-flow) or (conjugate flow)) near5 (code\$ or program\$) (h-flow or h flow or (conjugat\$ near4 flow)) and (pipeline\$ near5 execut\$)	0	<u>L4</u> <u>L3</u>
	(h-flow or h flow or (conjugat\$ near4 flow)) and (pipeline\$ near5		

## END OF SEARCH HISTORY



3 <u>Instruction issue logic for high-performance, interruptable pipelined processors</u> G. S. Sohi, S. Vajapeyam

June 1987 Proceedings of the 14th annual international symposium on Computer architecture

Full text available: pdf(814.60 KB)

Additional Information: full citation, abstract, references, citings, index

The performance of pipelined processors is severely limited by data dependencies. In order to achieve high performance, a mechanism to alleviate the effects of data dependencies must exist. If a pipelined CPU with multiple functional units is to be used in the presence of a virtual memory hierarchy, a mechanism must also exist for determining the state of the machine precisely. In this paper, we combine the issues of dependency-resolution and preciseness of state. We present a design for in ...

Code size reduction technique and implementation for software-pipelined DSP

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